# **CMOS System Reset IC with Separated Sence Line** Monolithic IC PST85XX

### **Outline**

This is a reset IC with an independent voltage detection monitor terminal VS and V<sub>DD</sub> terminal.

The IC power supply is separate and so, even if the monitor voltage VS is low, the output does not become unstable at the operation limit like conventional reset ICs. Instead it maintains low-level operation.

This IC is particularly suitable for low-voltage (1V type) power monitoring. It has an accuracy of ±1.5% and an ultralow current consumption of 0.35  $\mu A$  typ. and otherwise offers characteristics resembling those of conventional IC reset ICs.

#### **Features**

1. IC power supply terminal VDD and voltage-detecting monitor terminal VS are independent of each other.

2. High accuracy voltage detection 1.5% (Ta = 25°C)

3. Ultra low current consumption  $0.35 \,\mu\text{A typ}$ .

4. Low operating supply voltage 0.7~10.0V

-40~+105°C 5. Operating temperature range

### **Packages**

SSON-4

SOT-25A

SC-82AB

### **Applications**

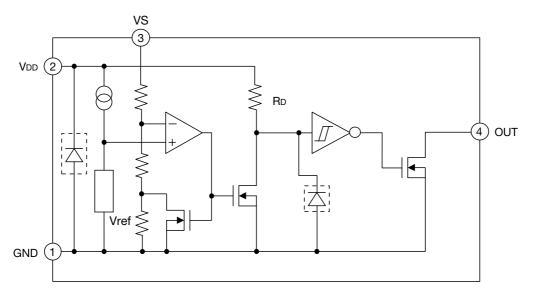
- 1. Reset circuits for CPUs and MPUs
- 2. Reset circuits for logic circuits
- 3. Battery voltage check circuits

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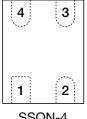
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## Block Diagram

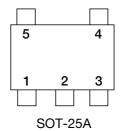
1-4 in the circuit diagram is pin number for the SC-82AB package.



## Pin Assignment



1	GND		
2	OUT		
3	VS		
4	$ m V_{DD}$		



(TOP VIEW)

1	OUT
2	$ m V_{DD}$
3	GND
4	NC
5	VS

SSC	DN-4
(TOP	VIEW)

3
2

SC-82AB (TOP VIEW)

1	GND		
2	$V_{ m DD}$		
3	VS		
4	OUT		

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## Pin Description

#### SSON-4

Pin No.	Pin name	Functions
1	GND	GND Pin
2	OUT	Reset Signal Output Pin
3	VS	Sence Pin
4	$V_{\mathrm{DD}}$	V <sub>DD</sub> Pin

#### SOT-25A

Pin No.	Pin name	Functions
1	OUT	Reset Signal Output Pin
2	$V_{\mathrm{DD}}$	Vdd Pin
3	GND	GND Pin
4	NC	No Connection
5	VS	Sence Pin

#### SC-82AB

Pin No.	Pin name	Functions
1	GND	GND Pin
2	$V_{\mathrm{DD}}$	V <sub>DD</sub> Pin
3	VS	Sence Pin
4	OUT	Reset Signal Output Pin

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## Absolute Maximum Ratings

Item	Symbol	Ratings	Units	
Supply voltage	Vdd max.	-0.3~+12.0	V	
Output voltage	OUT	-0.3~+12.0	V	
Output voltage	VS	-0.3~+12.0	V	
Input current (V <sub>DD</sub> )	Idd	20	mA	
Output current (RESET, RESET)	$I_{ m OUT}$	20	mA	
Dower dissination	PD	150(SOT-25A, SC-82AB)	mW	
Power dissipation		330(SSON-4)		
Operating temperature	Topr	-40~+105	°C	
Storage temperature	Tstg	-65~+150	°C	

# **Recommended Operating Conditions**

Item	Symbol	Ratings	Units	
Operating temperature	Topr	-40~+105	°C	
Supply voltage	$ m V_{DD}$	0.70~10.0	V	

### **Electrical Characteristics**

(Except where noted otherwise Ta=25°C)

Item	Symbol	Measurement conditions	Min.	Тур.	Max.	Units	Circuit
Reset threshold	V <sub>TH</sub>	V <sub>DD</sub> =V <sub>TH</sub> +1V Ta=+25° C Ta=-40~+85° C	0.8373	0.8500	0.8628	v	2
neset tillesiloid	VIH		0.8288		0.8713	•	4
Reset threshold hysteresis	⊿Vтн	$V_{DD}=V_{TH}+1V$ $VS=0V\rightarrow V_{TH}+1V\rightarrow 0V$	0.025	0.043	0.068	V	2
Supply current	Idd	$V_{\rm DD} \text{=} V_{\rm TH} \text{+} 1V$		0.35	1.0	μА	1
Reset threshold temp. coefficient	∠VTH/°C	Ta=-40~+85°C		±100		ppm/°C	2
"L" output current	I <sub>OL1</sub>	$\begin{array}{c} V_{\rm DD}\text{=}VS\text{=}0.7V,\\ V_{\rm DS}\text{=}0.05V \end{array}$	0.01	0.10		mA	3
VS Input Current	Is	V <sub>DD</sub> =VS=V <sub>TH</sub> +1V		100		nA	4
Output leakage current	Ileak	V <sub>DD</sub> =VS=10V, OUT=10V			0.1	μА	3

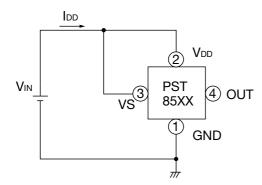
note1: This device is tested at Ta=25°C, over temperature limits guaranteed by design only.

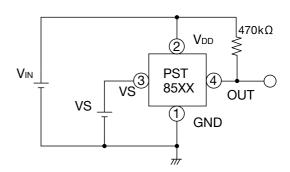
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## **Measuring Circuit**

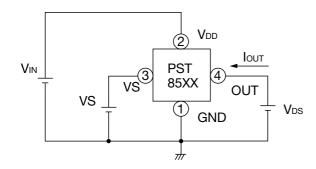
\*1)-4) in the circuit diagram is pin number for the SC-82AB package.

(1) (2)

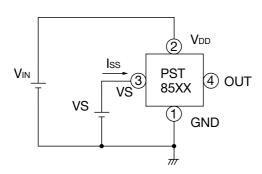




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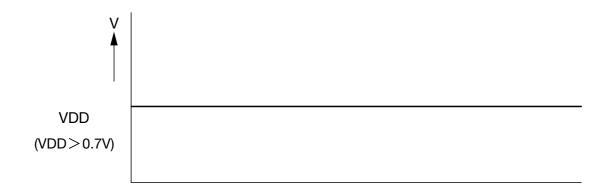


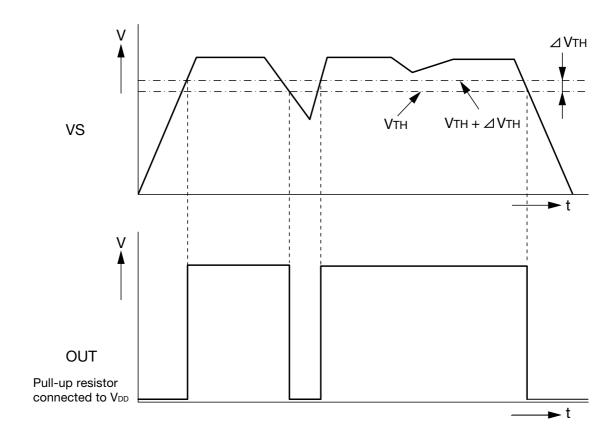
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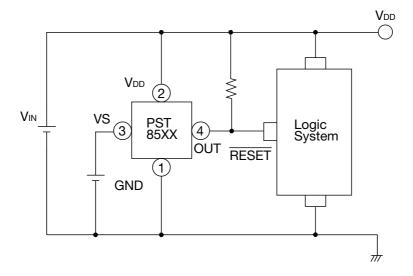
# **Timing Chart**





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# **Application Circuits**



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