

CMOS 300mA Regulator Monolithic IC MM329□□ Series

Outline

This IC is a low saturation regulator IC with 300mA output realizing low current consumption (45μA), low noise, and high ripple rejection using a CMOS process. The output capacitor is a ceramic capacitor, and the IC provides an ON/OFF control pin.

Features

- | | |
|---------------------------------|------------------------------------|
| 1. Low current consumption | 45μA |
| 2. High accuracy output voltage | ±2.0% |
| 3. Dropout voltage | 0.22V typ. (I _o =150mA) |
| 4. High ripple rejection | 70dB typ. |
| 5. Operating temperature range | −40 to +85°C |
| 6. Output voltage | 1.5 to 5.0V (0.1V steps) |
| 7. Output capacitor | 0.47μF (Ceramic) |

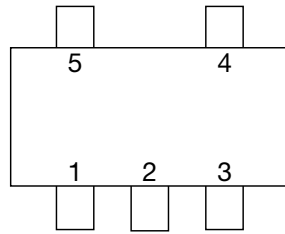
Package

1. SOT-25A
2. PLP-6A

Applications

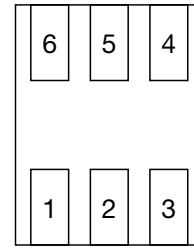
1. Constant voltage power supplies for battery-powered devices
2. Constant voltage sources for portable communication equipment
3. Constant voltage sources for home appliances

Pin Assignment



SOT-25A
(TOP VIEW)

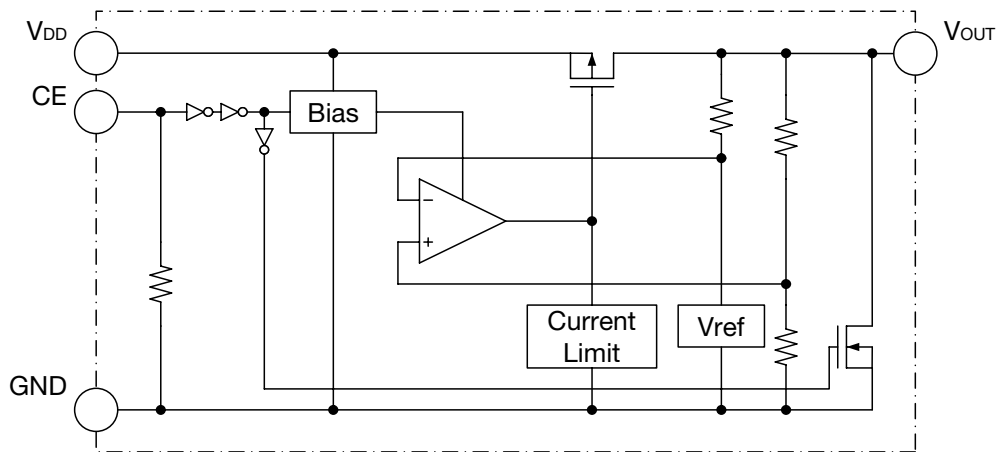
1	V _{DD}
2	GND
3	CE
4	NC
5	V _{OUT}



PLP-6A
(TOP VIEW)

1	V _{DD}
2	NC
3	V _{OUT}
4	NC
5	GND
6	CE

Block Diagram



Pin Description

Pin No.		Pin name	Function						
SOT-25A	PLP-6A								
1	1	V _{DD}	Voltage-supply pin						
4	2, 4	NC	No connection						
5	3	V _{OUT}	Output pin						
2	5	GND	Ground						
3	6	CE	ON/OFF-Control pin <table border="1" style="margin-left: 20px;"> <tr> <td>CE</td> <td>V_{OUT}</td> </tr> <tr> <td>L</td> <td>OFF</td> </tr> <tr> <td>H</td> <td>ON</td> </tr> </table> Connect CE pin with V _{DD} pin, when it is not used.	CE	V _{OUT}	L	OFF	H	ON
CE	V _{OUT}								
L	OFF								
H	ON								

Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Ratings		Units
Storage temperature	T _{STG}	-55~+150		°C
Supply voltage	V _{DD}	7.0		V
CE input voltage	V _{CE}	-0.3~V _{DD} +0.3		V
Output voltage	V _{OUT}	-0.3~V _{DD} +0.3		V
Output current	I _{omax}	350		mA
Power dissipation	Pd	SOT-25A	350mW *1	mW
		PLP-6A	280mW *2	

Note1: *1 With PC Board of glass epoxy 60×40×1.6mm

Note2: *2 With PC Board of glass epoxy 40×50×0.8mm

Recommended Operating Conditions (Ta=25°C)

Item	Symbol	Ratings	Units
Operating ambient temperature	T _{JOP}	-40~+85	°C
Operating voltage	V _{OP}	2~6	V
Output current	I _O	0~300	mA

Electrical Characteristics 1 (Except where noted otherwise, Ta=25°C, V_{DD}=V_{OUT} (typ.) +1V, V_{CE}=V_{DD})

Item	Symbol	Measurement conditions	Min.	Typ.	Max.	Units
Input current (OFF)	I _{DDoff}	V _{CE} =0V		0.1	1.0	μA
No-Load input current	I _{DD}	I _{OUT} =0mA		45	70	μA
Output voltage	V _{OUT}	1mA ≤ I _{OUT} ≤ 30mA	×0.98		×1.02	V
Line regulation	V _{LINE}	V _{DD} =V _O (Typ.) +0.5~6V, I _{OUT} =30mA (V _{OUT} ≤ 1.6V, V _{DD} =2.2~6V)		0.02	0.10	%/V
Load regulation	V _{LOAD}	1mA ≤ I _{OUT} ≤ 300mA		50	120	mV
Dropout voltage	V _{IO}	Please refer to Electrical Characteristics 2				V
Ripple rejection 1 *1	RR1	f=1kHz, V _{ripple} =0.5V, I _{OUT} =30mA (V _{OUT} ≤ 1.7V, V _{DD} =V _{OUT} +1.2V)		70		dB
Ripple rejection 2 *1	RR2	f=10kHz, V _{ripple} =0.5V, I _{OUT} =30mA (V _{OUT} ≤ 1.7V, V _{DD} =V _{OUT} +1.2V)		60		dB
V _{OUT} temperature coefficient *1	ΔV _{OUT} /ΔT	I _{OUT} =30mA -40 ≤ T _{OP} ≤ 85°C		±100		ppm/°C
Output Noise voltage *1	V _n	fBW=10~100kHz I _{OUT} =30mA		30		μV _{rms}
Output short-circuit current *1	I _{lim}	V _{OUT} =0V		40		mA
CE Pull-down resistance	R _{pd}		0.7	2	8	MΩ
CE high Threshold voltage	V _{CEH}		1.5		V _{DD}	V
CE low Threshold voltage	V _{CEL}		0		0.3	V
Output NMOS ON resistance	R _{DON}	V _{CE} =0V V _{DD} =4V (V _{OUT} <3V)		60		Ω

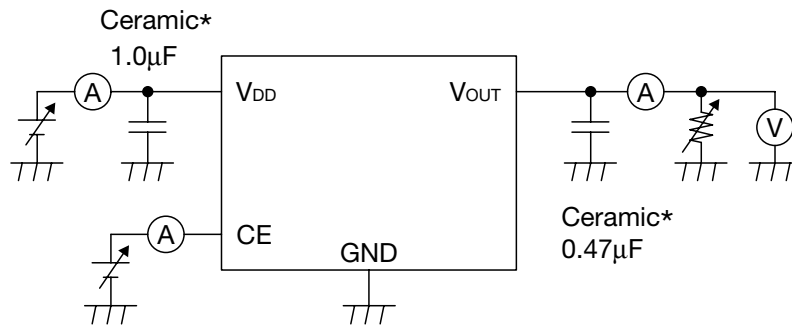
Note1: *1 The parameter is guaranteed by design.

Electrical Characteristics 2 (Except where noted otherwise, $T_a=25^\circ\text{C}$, $V_{DD}=V_{OUT}(\text{typ.})+1\text{V}$, $V_{CE}=V_{DD}$)

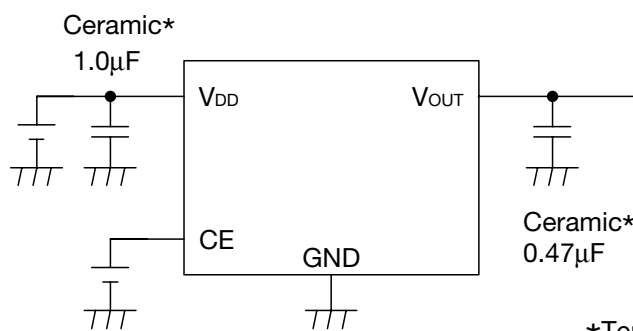
Model No.	Item							
	Output voltage				Dropout voltage			
	V_{OUT} (V)				V_{IO} (V)			
	Measurement conditions	Min.	Typ.	Max.	Measurement conditions	Min.	Typ.	Max.
MM3291C	$1\text{mA} \leq I_{OUT} \leq 30\text{mA}$	1.170	1.200	1.230	*			
MM3291D		1.270	1.300	1.330				
MM3291E		1.370	1.400	1.430				
MM3291F		1.470	1.500	1.530	$I_{OUT}=150\text{mA}$		0.38	0.70
MM3291G		1.568	1.600	1.632			0.36	0.65
MM3291H		1.666	1.700	1.734			0.34	0.60
MM3141J		1.764	1.800	1.836	$1.8\text{V} \leq V_{OUT} \leq 2.0\text{V}$		0.32	0.55
MM3291K		1.862	1.900	1.938				
MM3292A		1.960	2.000	2.040				
MM3292B		2.058	2.100	2.142	$2.1\text{V} \leq V_{OUT} \leq 2.7\text{V}$		0.28	0.50
MM3292C		2.156	2.200	2.244				
MM3292D		2.254	2.300	2.346				
MM3292E		2.352	2.400	2.448				
MM3292F		2.450	2.500	2.550				
MM3292G		2.548	2.600	2.652				
MM3292H		2.646	2.700	2.754				
MM3292J		2.744	2.800	2.856	$2.5\text{V} \leq V_{OUT} \leq 5.0\text{V}$		0.22	0.35
MM3292K		2.842	2.900	2.958				
MM3293A		2.940	3.000	3.060				
MM3293B		3.038	3.100	3.162				
MM3293C		3.136	3.200	3.264				
MM3293D		3.234	3.300	3.366				
MM3293E		3.332	3.400	3.468				
MM3293F		3.430	3.500	3.570				
MM3293G		3.528	3.600	3.672				
MM3293H		3.626	3.700	3.774				
MM3293J		3.724	3.800	3.876				
MM3293K		3.822	3.900	3.978				
MM3294A		3.920	4.000	4.080				
MM3294B		4.018	4.100	4.182				
MM3294C	4.116	4.200	4.284					
MM3294D	4.214	4.300	4.386					
MM3294E	4.312	4.400	4.488					
MM3294F	4.410	4.500	4.590					
MM3294G	4.508	4.600	4.692					
MM3294H	4.606	4.700	4.794					
MM3294J	4.704	4.800	4.896					
MM3294K	4.802	4.900	4.998					
MM3295A	4.900	5.000	5.100					

Note1: * The parameter is not guaranteed in the model less than $V_{OUT}=1.4\text{V}$.

Measuring Circuit



Application Circuit



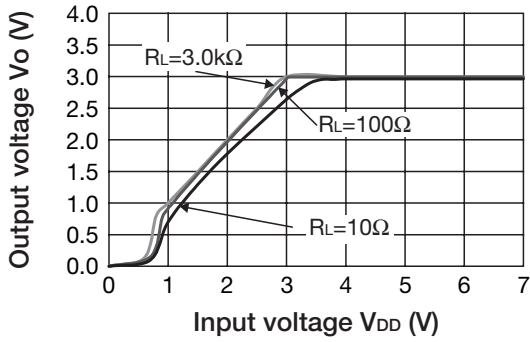
*Temperature Characteristics : B

Note

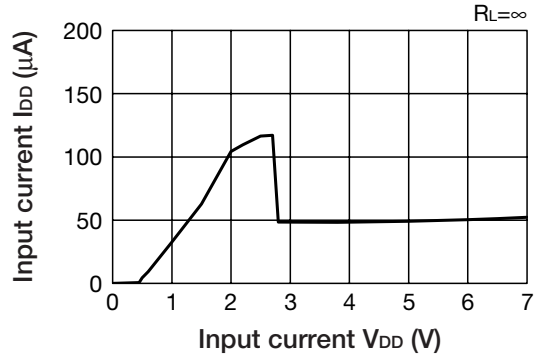
1. The output capacitor is required between output and GND to prevent oscillation.
2. The output capacitor must be used in ESR stable area.
It is possible to use a ceramic capacitor without ESR resistance for output.
The ceramic capacitor must be used more than 1.0µF or 0.47µF and B temperature characteristics.
3. The wire of V_{CC} and GND is required to print full ground plane for noise and stability.
4. The input capacitor must be connected in 1cm from the input pin.
5. In case the output voltage is above the input voltage, the overcurrent flows by internal parasitic diode from output to input.

Characteristics ($V_o=3.0V$ Except where noted otherwise, $T_a=25^\circ C$, $V_{DD}=V_{OUT} (typ.) +1V$, $V_{CE}=V_{DD}$)

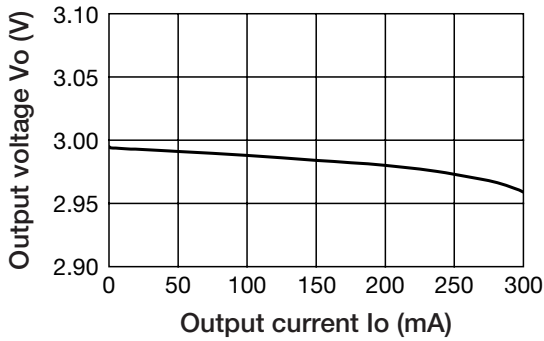
Output-Input Voltage



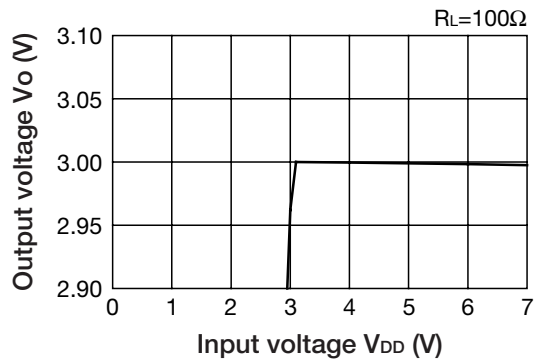
Input Current-Input Voltage



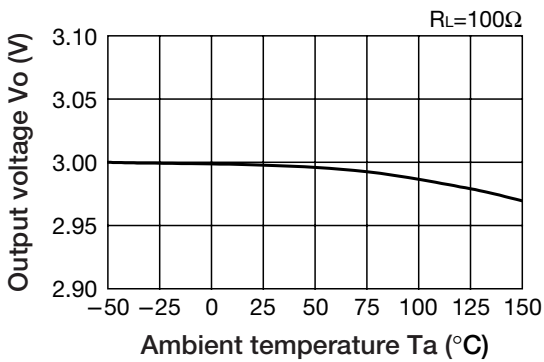
Load regulation



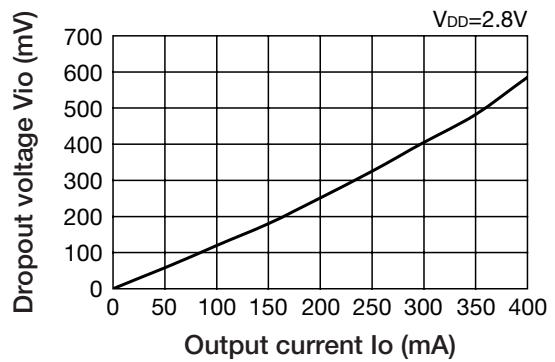
Line Regulation



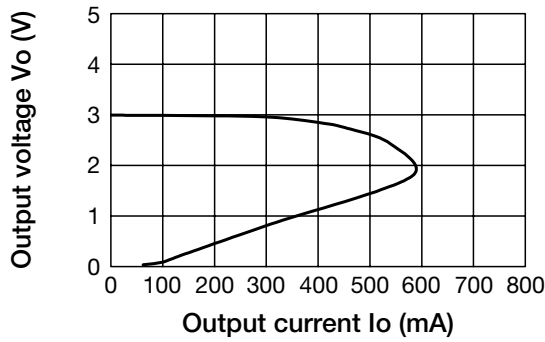
Output Voltage-Ambient Temperature



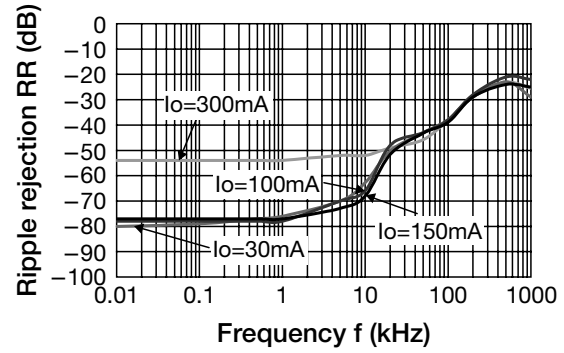
Dropout Voltage



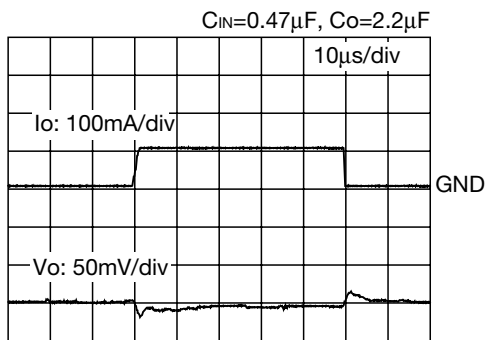
■ Output Voltage-Output Current



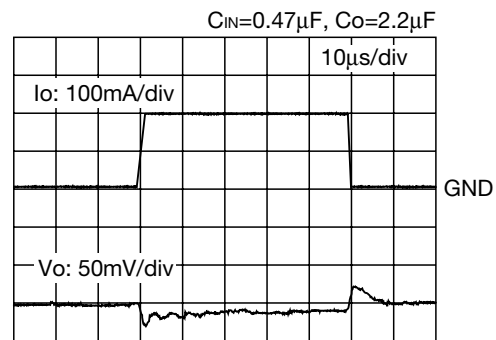
■ Ripple Rejection



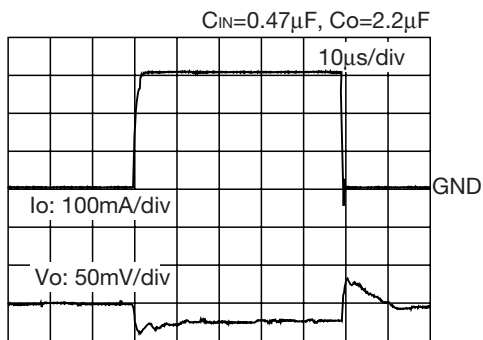
■ Load transient response (Io=10 → 100mA)



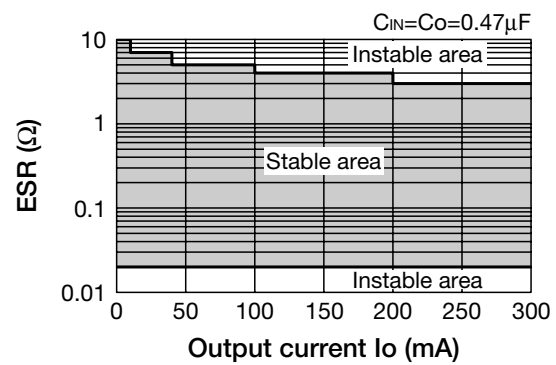
■ Load transient response (Io=10 → 150mA)



■ Load transient response (Io=10 → 300mA)



■ ESR Stable Area



Characteristics (Except where noted otherwise, $T_a=25^\circ\text{C}$, $V_{DD}=V_{OUT}(\text{typ.})+1\text{V}$, $V_{CE}=V_{DD}$)

Turn-On & Turn-Off Transient Responses

