

# High Precision Operational Amplifier Monolithic IC MM1278

## Outline

This IC is a dual-type operational amplifier, with two built-in operational amplifiers, and achieves extremely low offset voltage with a single power supply. The input offset voltage and the input offset voltage temperature drift of this operation amplifier are one digit less than those of our conventional products. A single power supply can be used, so there is no need to create a mid-point potential, and it can be operated with the voltage from two batteries. Due to the single power supply, low current consumption, and low offset voltage, two batteries can be used for operation, making it appropriate for equipment that amplifies the very small signal of portable equipment.

## Features

### General

- |  |  |
|--|--|
| 1. Power supply voltage                | $V_{CC}=1.8V\sim 6V$<br>optimum for drive by batteries |
| 2. Current consumption                 | 0.1mA typ.   |
| 3. Power supply line elimination ratio | 60dB typ.  |

### Amplifier section

- |                                   |                            |
|-----------------------------------|----------------------------|
| 1. Input voltage range            | $-0.2V\sim +0.3V$          |
| 2. Super low input offset voltage | $\pm 0.1mV$ typ.           |
| 3. Input offset voltage           | $\pm 1\mu V/^\circ C$ typ. |
| 4. Input offset current           | 1nA typ.                   |
| 5. Input bias current             | 50nA typ.                  |
| 6. Voltage gain                   | 100dB typ.                 |

## Package

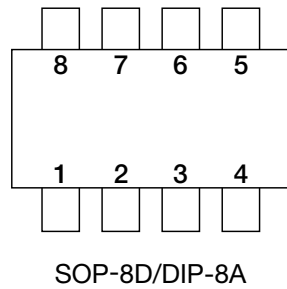
- SOP-8D (MM1278XF)
- DIP-8A (MM1278XD)

## Applications

1. Amplification and detection of normal very small voltage on portable equipment.
2. Amplification of very small voltage for sensors (thermocouples, strain gauges, magnetic sensors)
3. Detedion of very small current

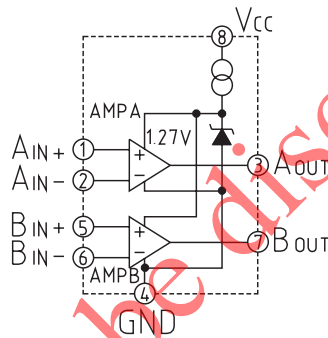
Pin Assignment

MM1278XF, MM1278XD



1	A <sub>IN+</sub>
2	A <sub>IN-</sub>
3	A <sub>OUT</sub>
4	GND
5	B <sub>IN+</sub>
6	B <sub>IN-</sub>
7	B <sub>OUT</sub>
8	V <sub>CC</sub>

Block Diagram



Absolute Maximum Ratings (T<sub>a</sub>=25°C)

Item	Symbol	Rating	Units
Storage temperature	T <sub>STG</sub>	-40~+125	°C
Operating temperature	T <sub>OPR</sub>	-20~+85	°C
Power supply voltage	V <sub>CC max.</sub>	-0.3~+10	V
Input voltage	V <sub>IN</sub>	-0.3~+10	V
Allowable power dissipation	P <sub>d</sub>	300	mW

**Electrical Characteristics** (Unless otherwise specified Ta=25°C, VCC=3V, VIN=0V)

Item	Symbol	Measurement Conditions	Min.	Typ.	Max.	Units	
Current consumption	ICC			0.1	0.15	mA	
Power supply line elimination ratio	PSRR	f=100Hz	50	60		dB	
Power supply voltage range	VCC		1.8	3.0	6.0	V	
Amplifier section	Input voltage range	VI	-0.2		0.3	V	
	Input offset voltage	VIOA		±0.1	±0.35	mV	
	Input offset voltage temperature drift	ΔVIOA	Ta=-20~+70°C		±1	±3	μV/°C
	Input offset current	IIOA		1	10	nA	
	Input bias current	IBA		50	150	nA	
	Voltage gain	GV	RL=100kΩ	80	100		dB
	Output current	IOA	VIN=10mV, VO=0.5V	0.5			mA
	Output voltage	VOA	VIN=-5~-25mV RL=10kΩ	0.01		VCC -1.0	V

Note : Use a gain range of 20dB ~ 50dB in the amplifier section (40dB recommended).

Also, insert the capacitor between output and GND, instead of parallel to the gain setting feedback resistor, in order to eliminate noise from the input.

Please note that use of the equipment under different conditions will cause vibration.

Products to be discontinued